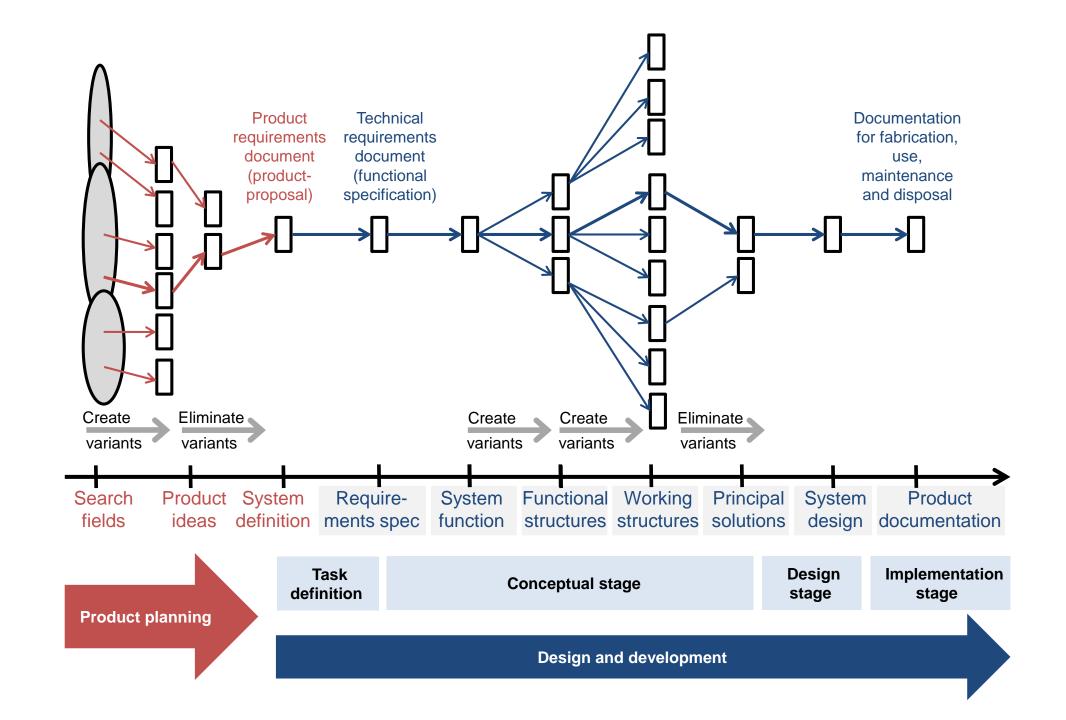
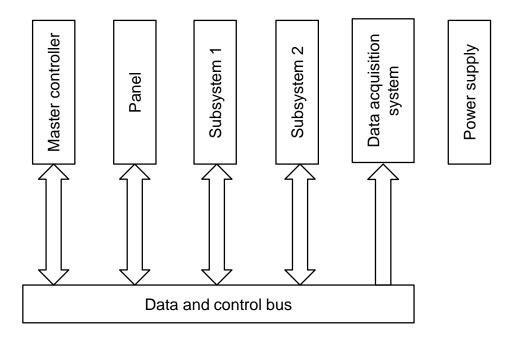
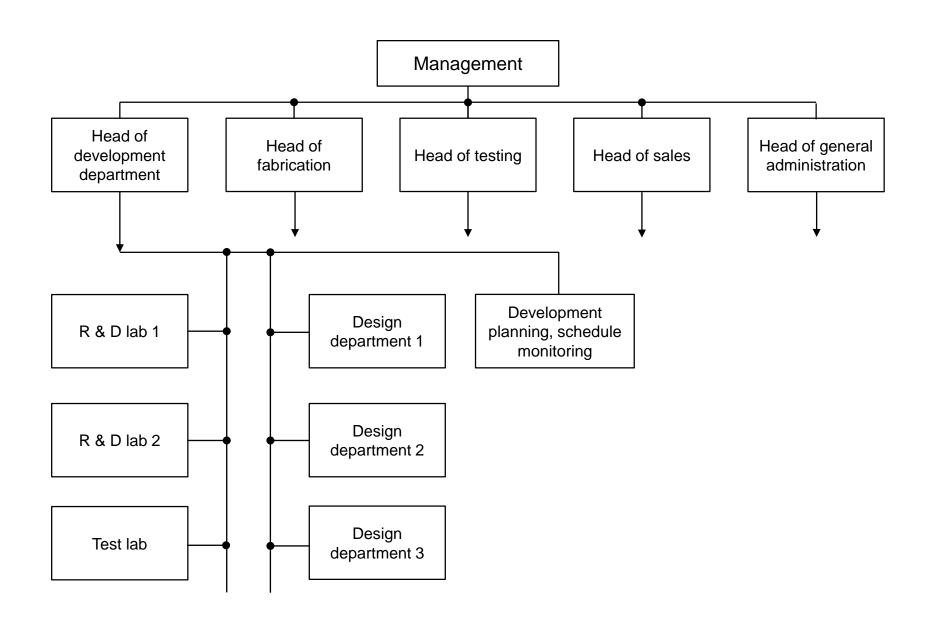
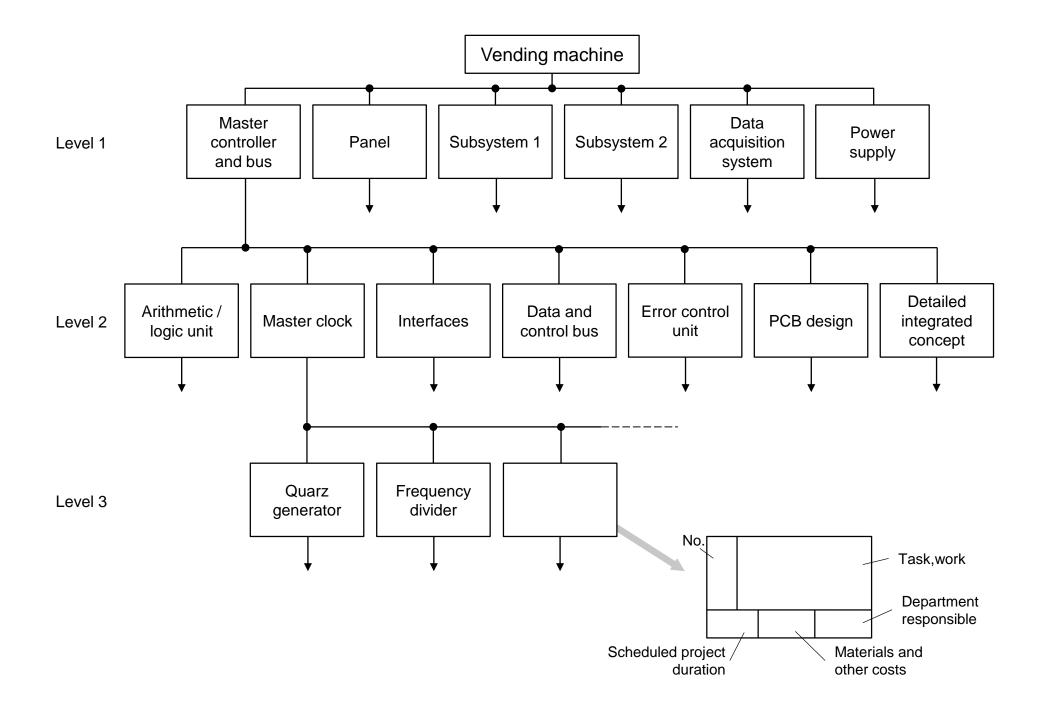


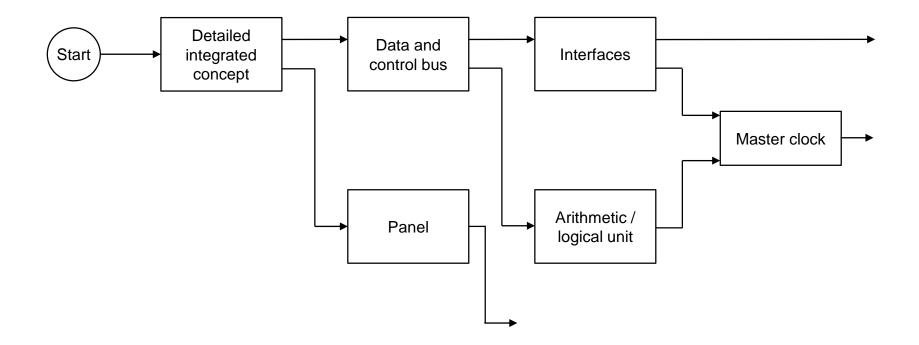
© J. Lienig, H. Bruemmer, *Fundamentals of Electronic Systems Design*. Springer, ISBN 978-3-319-55839-4, 2017. Figures of Chapter 2: Design Process and Its Fundamentals



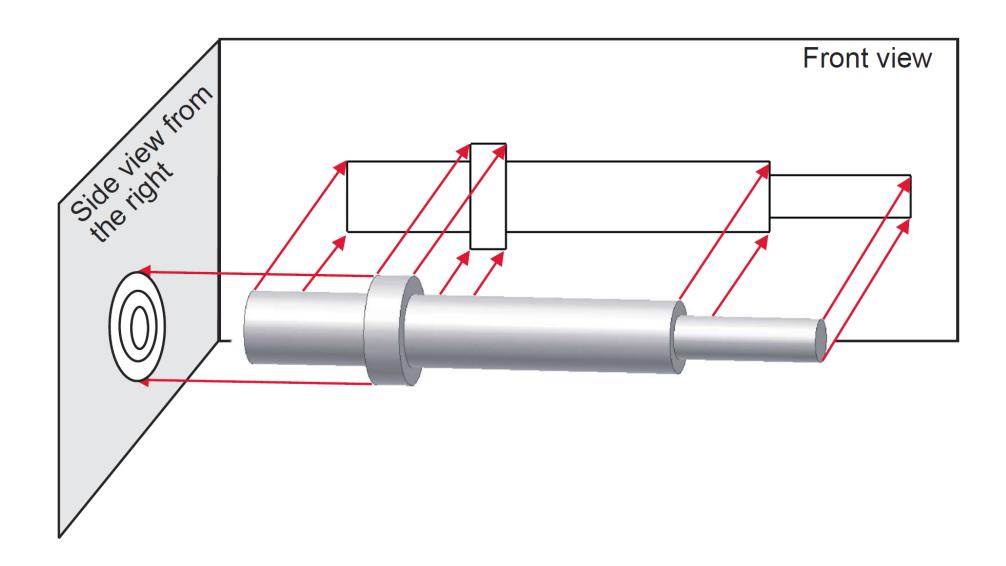


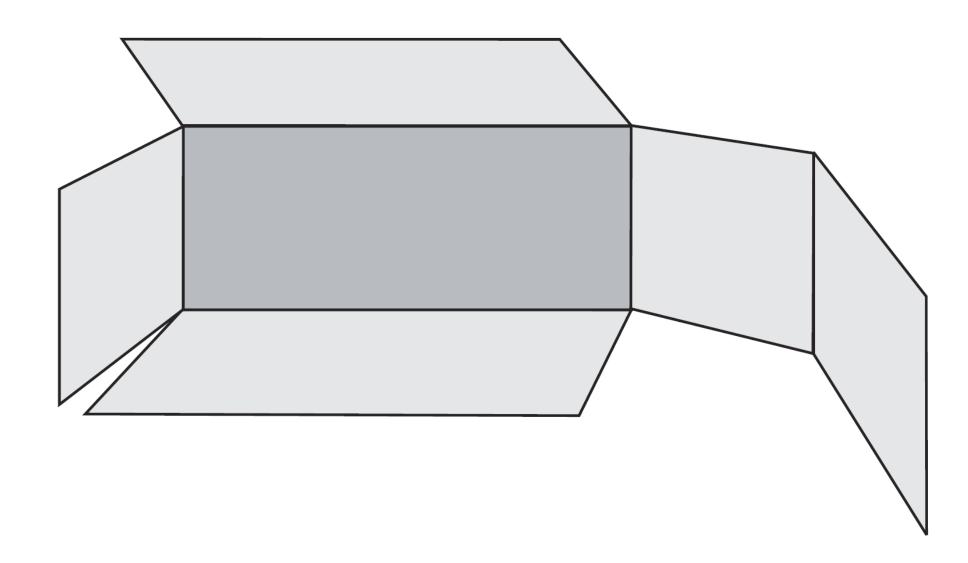


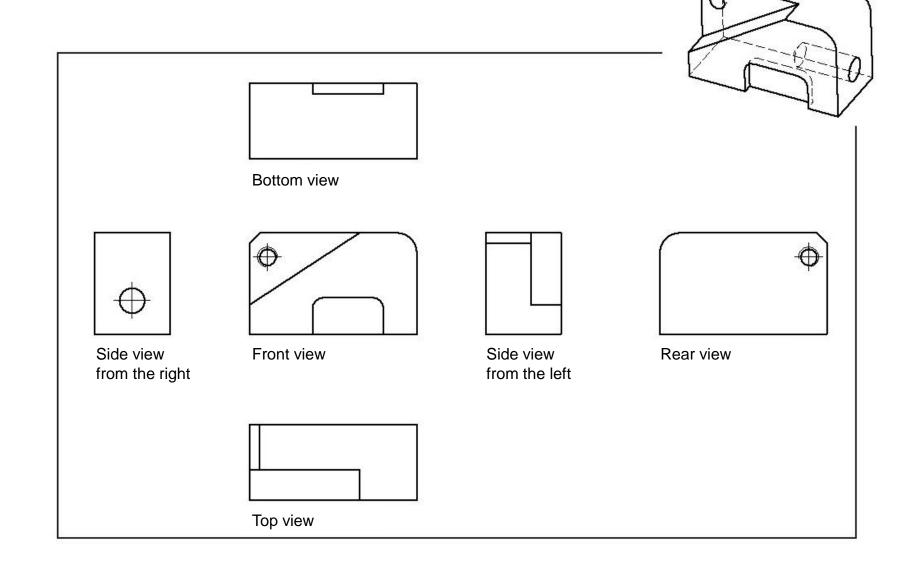


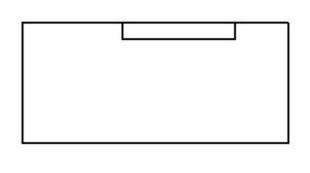


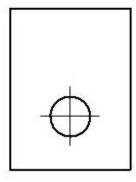
No.	Calendar week	7	8	9	10	11	12	13	14	15	16	
	Description	•)	J	10			10			10	
1	Detailed integrated concept											
2	Data and control bus											
3	Interfaces											
4	Master clock											
5	Arithmetic / logical unit											
6	Panel											

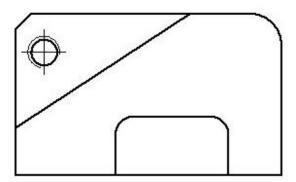


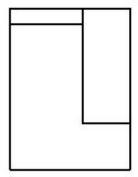


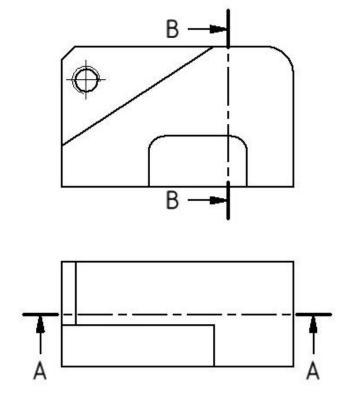


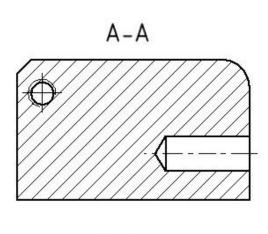


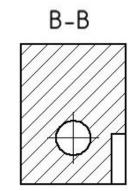


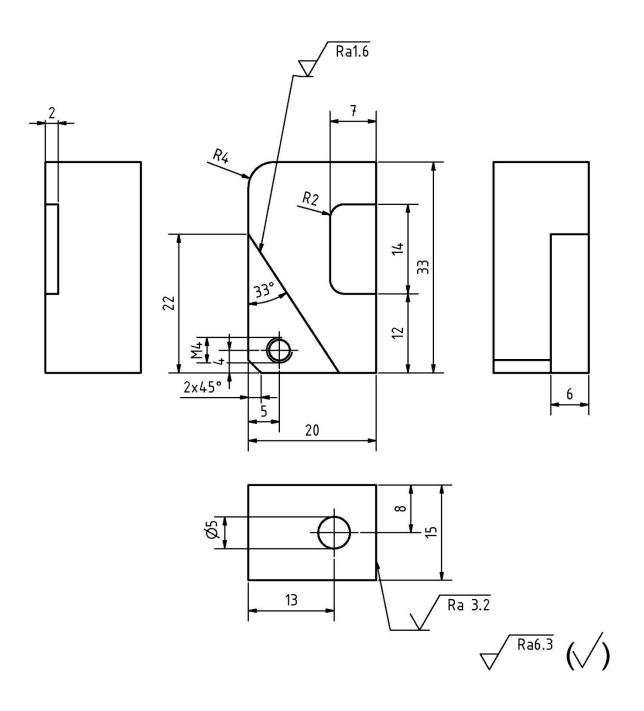


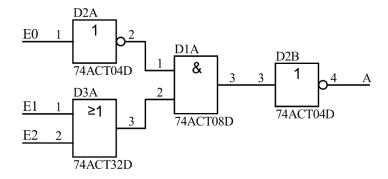


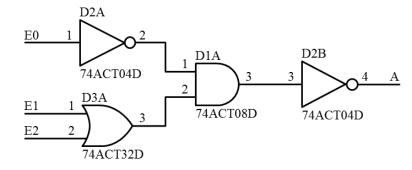


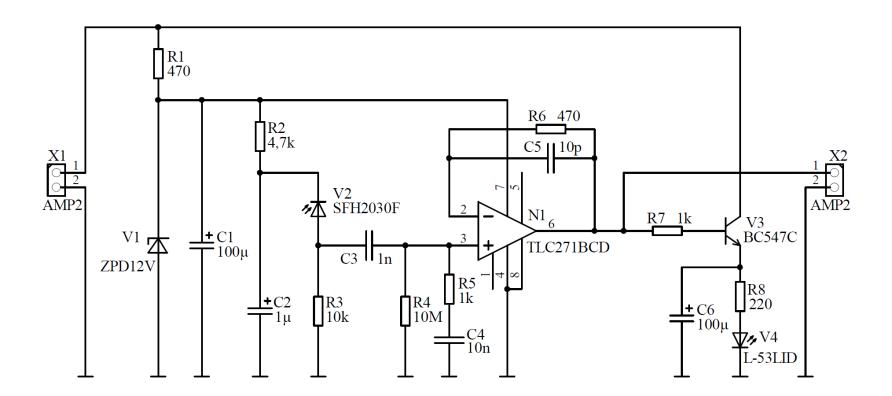


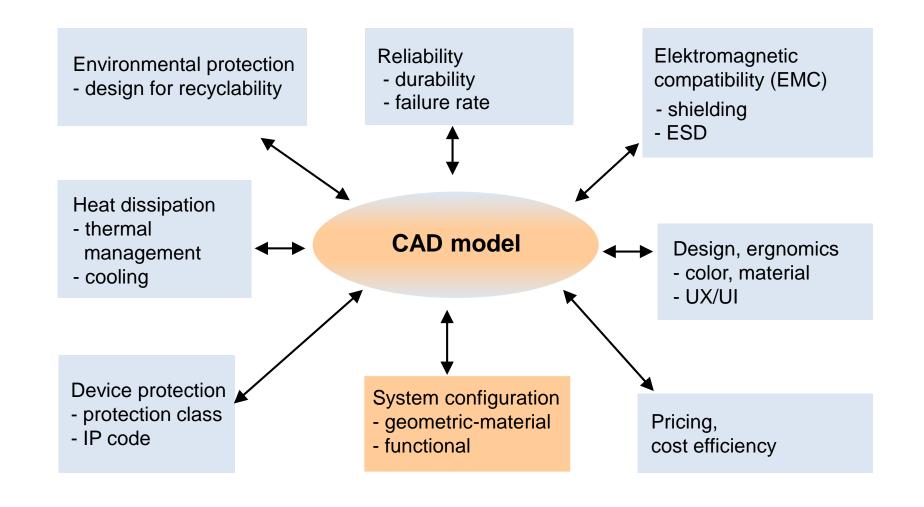




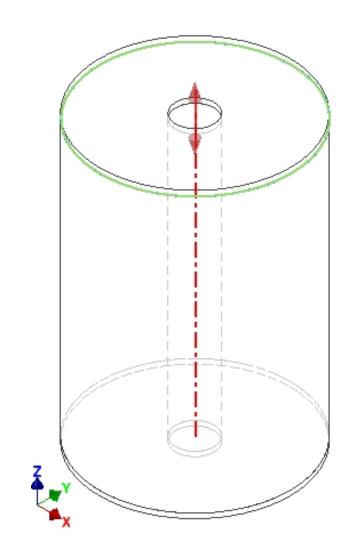


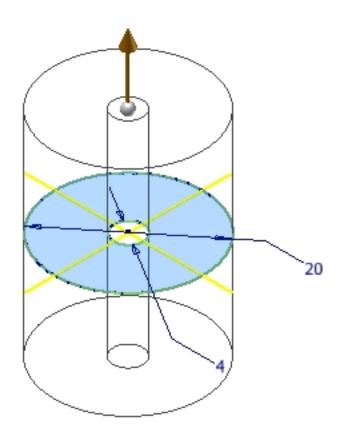




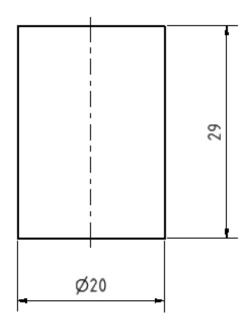


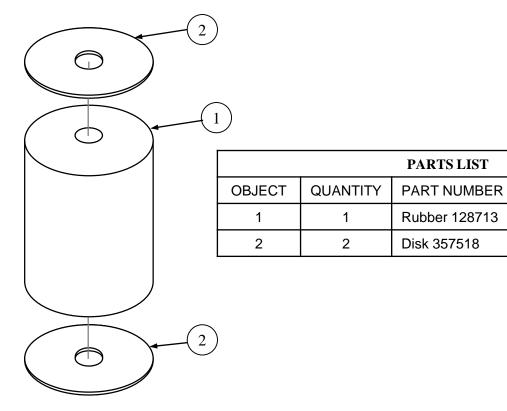












DESCRIPTION

Rubber sleeve

Carbon steel disk

