Einladung zum Forschungsseminar

Thema: **AI for Chip Design**

Vortragender: **Prof. Jiang Hu, Department of Electrical and Computer Engineering, Texas A&M University, College Station, TX, USA**

Leitung: **Prof. Dr.-Ing. habil. Jens Lienig**

Zeit / Ort: **6. Juni 2025, 14 Uhr, BAR II/26**

Over the past decade, artificial intelligence (AI) and machine learning (ML) have profoundly transformed the field of chip design. These technologies have not only significantly improved existing design automation methods but have also enabled the development of new techniques in previously unexplored areas.

This 30-minute presentation provides an overview of recent advances and the state-of-the-art in AI-driven chip design, including advancements in chip design prediction, functional verification acceleration, automated tool parameter tuning, and analog design automation. It also examines the key challenges and limitations that AI/ML currently face in this domain. Finally, the talk will offer a forward-looking perspective on future trends and opportunities in this evolving field.
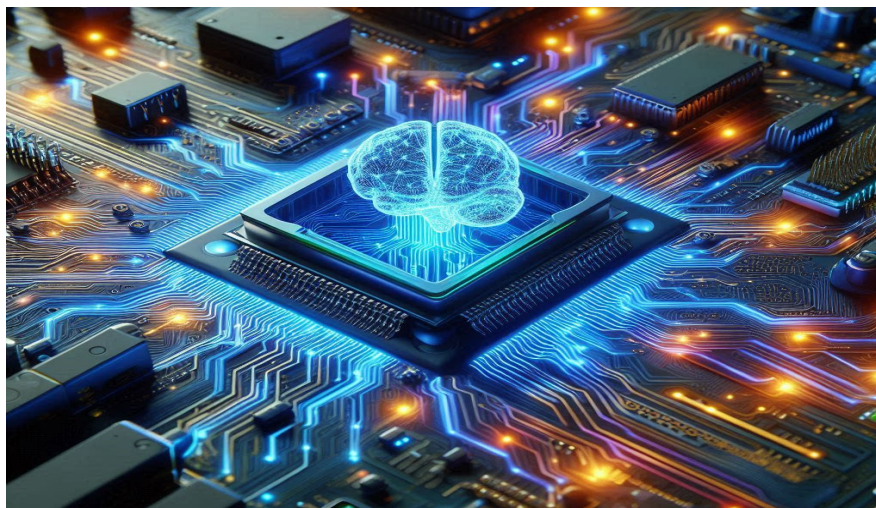
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Speaker Bio: Jiang Hu earned his Ph.D. in Electrical Engineering from the University of Minnesota in 2001, following his M.S. in Physics in 1997. He previously completed his B.S. in Optical Engineering at Zhejiang University, China, in 1990. Currently, he holds the prestigious title of Eric D. Rubin Professor in the Department of Electrical and Computer Engineering at Texas A&M University. His research interests include electronic design automation, machine learning applications, approximate computing, computer architecture and hardware security. He has co-authored more than 270 technical papers, co-invented 10 patents and co-edited a book. He is also a Hans Fischer Senior Fellow visiting the Technical University of Munich.